



FOR IMMEDIATE RELEASE

EV GROUP SHOWCASES NEW HYBRID DIE-TO-WAFER BONDING ACTIVATION SOLUTION TO SPEED DEPLOYMENT OF 3D-IC / HETEROGENEOUS INTEGRATION AT SEMICON CHINA

EVG®320 D2W die preparation and activation system provides seamless integration with third-party die bonders; EVG also partners with ASM Pacific Technology on die-to-wafer hybrid bonding solutions

ST. FLORIAN, Austria, March 10, 2021—EV Group (EVG), a leading supplier of wafer bonding and lithography equipment for the MEMS, nanotechnology and semiconductor markets, has introduced the EVG®320 D2W die preparation and activation system—the industry’s first commercially available hybrid bond activation and cleaning system for die-to-wafer (D2W) bonding. The system incorporates all critical pre-processing modules needed for D2W bonding, including cleaning, plasma activation, die alignment verification and other essential metrology, and can be operated as a stand-alone system or integrated with third-party pick-and-place die bonding systems.

Leveraging EVG’s decades of experience in hybrid bonding technology, the EVG320 D2W fills a critical need for innovative process solutions that can accelerate the deployment of heterogeneous integration and enable new generations of devices and systems such as high-bandwidth memory (HBM), logic-on-memory, chiplets, segmented and 3D system on chip (SoC) devices, and 3D stacked backside illuminated CMOS image sensors. EVG will showcase this breakthrough D2W bonding solution at the SEMICON China exhibition, to be held March 17-19 at the Shanghai New International Expo Centre.

Hybrid Bonding is an Enabling Process for Heterogeneous Integration

Leading-edge applications such as artificial intelligence (AI), autonomous driving, augmented/virtual reality (AR/VR) and 5G require the development of high-bandwidth, high-performance and low-power-consumption devices without increasing production cost. As traditional 2D silicon scaling reaches its cost limits, the semiconductor industry is turning to heterogeneous integration—the manufacturing, assembly and packaging of multiple different components or dies with different feature sizes and materials onto a single device or package—in order to increase performance on new device generations.

Wafer-to-wafer (W2W) hybrid bonding, which involves stacking and electrically connecting wafers from different production lines, is a central process in heterogeneous integration and has a proven track record of success for certain applications such as CMOS image sensors and various memory and logic technologies. However, in cases where the components or dies are not the same size, D2W hybrid bonding offers a viable pathway to enabling heterogeneous integration. With its new D2W bonding solution, extensive experience drawn from its market-leading W2W hybrid bonding solutions and industry collaborations supported by its Heterogeneous Integration Competence Center™, EVG is well positioned to support D2W bonding applications.



EV GROUP SHOWCASES DIE-TO-WAFER BONDING AT SEMICON CHINA 2021.....Page 2 of 3

“For 20 years, EVG has continually set new standards for wafer-to-wafer hybrid and fusion bonding with the largest installed base of wafer bonding solutions worldwide,” stated Paul Lindner, executive technology director of EV Group. “We have already begun serving the needs of the emerging die-to-wafer market with a dedicated version of our established EVG GEMINI® FB system specially configured for die-to-wafer bonding. The new EVG320 D2W die preparation and activation system adds to our expertise in die-to-wafer bonding and completes EVG’s equipment portfolio for providing an end-to-end hybrid bonding solution to accelerate the deployment of 3D-IC/heterogeneous integration. In the future, we anticipate that both wafer-to-wafer and die-to-wafer process flows will be required and run in parallel in numerous fabs for heterogeneous integration applications, providing many opportunities for EVG to support this critical and rapidly growing technology landscape.”

Die-to-Wafer Process Flow

Several different D2W bonding methods are available and are selected depending upon the application and customer requirements. In collective D2W (Co-D2W) bonding, singulated dies are placed on collective die carriers and then are transported to a target wafer for die transfer where a W2W hybrid or fusion bonding system, such as the GEMINI FB, are used to bond the dies to the target wafer. In direct placement D2W (DP-D2W) bonding, the singulated dies are bonded to the target wafer one by one using a pick-and-place flip-chip bonder. Plasma activation and cleaning of the surfaces of the dies on the handler wafer are essential steps for establishing a high-yielding bond and electrical interface between the dies and target wafer. This is where the EVG320 D2W activation system comes in.

ASM Pacific Technology and EVG Join Forces on Die-to-Wafer Hybrid Bonding

In a related development, ASM Pacific Technology (ASMPT) and EVG recently announced the signing of a joint development agreement (JDA) to co-develop die-to-wafer hybrid bonding solutions for 3D-IC/heterogeneous integration applications. Both companies are leaders in their respective areas, with EVG’s die preparation technology and front-end cleanliness for die-to-wafer hybrid bonding, and ASMPT’s ultra-high precision bonding of extremely thin dies. Under the JDA, ASMPT will provide its precision die-bonding capabilities, while EVG will provide its die preparation (cleaning and activation) and wafer bonding capabilities in the form of the EVG320 D2W system for direct-placement die-to-wafer bonding as well as the GEMINI FB configured for collective die-to-wafer integration flows.

Commenting on the JDA, Mr. Nelson Fan, Vice President for Advanced Packaging, BU ICD and CIS, Semiconductor Solutions at ASMPT, stated, “We are honored and privileged to further extend our partnership with EVG, a pioneering leader in wet process technologies for wafer hybrid bonding. We aim to deliver the next evolution of IC interconnect solutions that will include our ultra-high precision LithoBolt™ hybrid bonder for chip-to-wafer hybrid bonding and this will complement our total interconnect solutions for heterogeneous integration.”

More information on the ASMPT-EVG JDA is available at

<https://www.evgroup.com/company/news/detail/asm-pacific-technology-and-ev-group-join-forces-to-enable-industrys-first-ultra-precision-die-to-wafer-hybrid-bonding-solutions-for-3d-ic-heterogeneous-integration/>.



Product Details

The EVG320 D2W is a highly flexible platform that features a universal hardware/software interface to enable seamless integration with third-party pick-and-place die bonding systems. It also can operate as a stand-alone system depending on integration and line balancing requirements. The system incorporates EVG's advanced cleaning and plasma activation technology, which is available across its industry-standard W2W fusion and hybrid bonding platforms and has been proven in hundreds of installed modules worldwide. In addition, the EVG320 D2W features EVG's alignment verification module (AVM), an integrated metrology module that provides direct feedback to the die bonder on critical process parameters, such as die placement accuracy and die-height information as well as post-bond metrology, for improved process control. Additional features include flexible substrate handling that can accommodate any type of die carrier or film frame that can support plasma activation, hybrid and fusion bonding cleanliness standards, and SECS/GEM standard support.

EV Group at SEMICON China

EVG will showcase its suite of heterogeneous integration manufacturing solutions, including the EVG320 D2W and GEMINI FB systems, along with its complete suite of wafer bonding, lithography and resist processing solutions, at SEMICON China. Attendees interested in learning more can visit EVG at Booth #2387 in Hall N2 of the Shanghai New International Expo Centre.

Product Availability

EVG is now accepting orders for the new EVG320 D2W die preparation and activation system, and offering product demonstrations at EVG's [Heterogeneous Integration Competence Center](#) at its headquarters in Austria. For more information, please visit <https://www.evgroup.com/products/bonding/die-to-wafer-bonding-systems/evg320d2w/>.

About EV Group (EVG)

EV Group (EVG) is a leading supplier of equipment and process solutions for the manufacture of semiconductors, microelectromechanical systems (MEMS), compound semiconductors, power devices and nanotechnology devices. Key products include wafer bonding, thin-wafer processing, lithography/nanoimprint lithography (NIL) and metrology equipment, as well as photoresist coaters, cleaners and inspection systems. Founded in 1980, EV Group services and supports an elaborate network of global customers and partners all over the world. More information about EVG is available at www.EVGroup.com.

Contacts:

Clemens Schütte
Director, Marketing and Communications
EV Group
Tel: +43 7712 5311 0
E-mail: Marketing@EVGroup.com

David Moreno
Principal
Open Sky Communications
Tel: +1.415.519.3915
E-mail: dmoreno@openskypr.com

###