

#### FOR IMMEDIATE RELEASE

# EV GROUP PARTNERS WITH NSI TO ENABLE FIRST WAFER-LEVEL HETEROGENEOUS INTEGRATION OF GAAS ON SILICON FOR RF FRONT-END MODULE MANUFACTURING

NSI's proprietary micro wafer-level system integration (uWLSI®) technology, combined with EVG's wafer bonding and lithography systems, enables most compact RF front-end chipsets for 4G/5G handsets

**SEMICON CHINA, March 20, 2019**—EV Group (EVG), a leading supplier of wafer bonding and lithography equipment, today announced that it has partnered with Ningbo Semiconductor International Corporation (NSI), a specialty semiconductor foundry based in Ningbo, China, in the development of the industry's first process technology platform for wafer-level heterogeneous integration of gallium arsenide (GaAs) on silicon for use in RF front-end module (FEM) manufacturing. This marks an important milestone in the development of next-generation, high-performance, ultra-compact RF front-end chipsets that are needed for 4G and 5G smartphones and other handsets.

As part of this strategic collaborative effort, EVG provided NSI with its industry-leading temporary bonding/debonding (TB/DB), permanent bonding, mask alignment lithography, and related special metrology equipment and process expertise, which NSI leveraged together with its proprietary micro wafer-level system integration (uWLSI®) technology platform to produce the innovative RF FEM products for a leading RF front-end devices and system solution provider. NSI is a joint venture subsidiary of Semiconductor Manufacturing International Corporation (SMIC) with China IC Investment Fund, Ningbo Economic Development Zone Industrial Investment Company, Ltd. and other IC investment funds.

According to Dr. Herb Huang, CEO of NSI, "High-performance, ultra-compact RF front-end microsystem components are vital to the success of the 5G wireless terminal. To support the customers and their RF front-end module products of the next generation of wireless terminals for meeting the stringent requirements of lower insertion loss, higher energy efficiency, ultra-miniaturization, etc., it is necessary to provide more advanced wafer-level multi-chip heterogeneous integration process solutions for supporting and helping the customer meet the product standards and quickly ramp to volume production. We are pleased to have access to EV Group's leading wafer bonding technology, lithography technology and expertise in heterogeneous integration in order to facilitate the manufacturing success."

## Wafer-level Heterogeneous Integration Needed for 5G

RF FEMs include several key components, such as power amplifiers (PAs), antenna switches and filters. The high-density 3D heterogeneous integration of materials with different properties, such as GaAs and silicon, is effective in enabling improved gain, linearity and power performance for RF FEMs. At the same time, however, the migration to 5G broadband wireless technology is driving the need for wider multi-band PAs and more RF filters in the FEM, which in turn can drive up cost and footprint of the overall chipset package. Enabling heterogeneous system integration at the wafer level provides a cost-effective approach for achieving greater chipset density with minimal footprint increase.

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"5G is offering huge opportunity to the RF front end industry," stated Cédric Malaquin, technology and market analyst, RF Devices and Technology, at market research and strategy consulting company Yole Développement (Yole). "5G will completely redefine the interactions between the network and the modem. Indeed, new RF bands, sub-6 GHz and mm-wave pose big challenges for the industry." According to Yole, the mobile handset RF front-end market, along with the WiFi connectivity sector, is projected to reach US\$35.2 billion in 2023 at a CAGR of 14% (1).

uWLSI<sup>®</sup> is a middle-end-of-line technology platform for a unique wafer fabrication process developed by NSI for enabling heterogeneous multi-die-on-wafer system integration and wafer-level system testing while eliminating the need for bumping and flip-chip processes in typical system-in-package practices. NSI developed the uWLSI<sup>®</sup> technology platform to specifically address the surging need for high-density heterogeneous system integration of a variety of chipsets and microsystems through more wafer-level fabrication processing.

EVG's TB/DB systems play a key role in enabling the heterogeneous integration of compound semiconductors with silicon devices in a More-than-Moore approach. For example, TB/DB facilitates the reliable transfer and processing of very thin compound semiconductor, silicon and mold wafers, which can be integrated into higher-density vertical packages. Likewise, EVG's mask alignment systems support wafer-level heterogeneous integration by enabling lithography patterning of carrier mounted and warped substrates, which is essential for the uWLSI process.

"NSI is at the forefront in the development of specialty semiconductors for next-generation wireless and telecommunications technologies, such as 5G," according to Swen Zhu, general manager, China, EV Group. "EVG is pleased that NSI has entrusted us once again to work with them to support their advanced manufacturing efforts—this time on their ground-breaking development in RF FEM platforms. The expertise that we have built up as a technology and market leader in wafer bonding and lithography process solutions will play an important role in our work with this leading foundry company."

EVG will showcase its latest wafer bonding and lithography solutions for heterogeneous integration and other applications at SEMICON China, a leading exhibition for China's semiconductor industry, which will be held this week (March 20-22) at the Shanghai International Expo. Attendees interested in learning more can visit EVG at Booth #2547 in Hall N2.

(1) Source: <u>5G's Impact on RF Front-End Module and Connectivity for Cell Phones report</u>, Yole Développement, 2018

#### **About NSI**

Ningbo Semiconductor International Corporation (NSI) is a joint venture subsidiary of Semiconductor Manufacturing International Corporation (SMIC) with China IC Investment Fund, Ningbo Economic Development Zone Industrial Investment Company, Ltd. and other IC investment funds. Through acquisition, adoption, enhancement and development of intellectual property and technology, NSI develops analog and specialty semiconductor technology platforms in the areas of high-voltage analog, radio frequency, and optoelectronics. These developments support customers in IC design and product development for applications in smart home, industrial, and automotive electronics, new generations of radio communications, AR/VR/MR, and other specialty systems. For more information on NSI, please contact: <a href="mailto:contact@nsemii.com">contact@nsemii.com</a>.

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# **About EV Group (EVG)**

EV Group (EVG) is a leading supplier of equipment and process solutions for the manufacture of semiconductors, microelectromechanical systems (MEMS), compound semiconductors, power devices and nanotechnology devices. Key products include wafer bonding, thin-wafer processing, lithography/nanoimprint lithography (NIL) and metrology equipment, as well as photoresist coaters, cleaners and inspection systems. Founded in 1980, EV Group services and supports an elaborate network of global customers and partners all over the world. More information about EVG is available at <a href="https://www.EVGroup.com">www.EVGroup.com</a>.

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